

Notice of References Cited		Application/Control No. 09/517,345	Applicant(s)/Patent Under Reexamination ANDERSON ET AL.	
		Examiner Nitin Parekh	Art Unit 2811	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	John H. Lau, "Chip on Board Technologies for Multichip Modules", 1994, Published by Chapman & Hall, pp. 52, 474-476 and 504)
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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